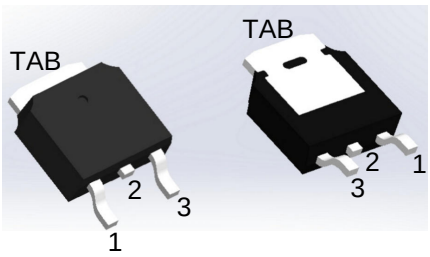
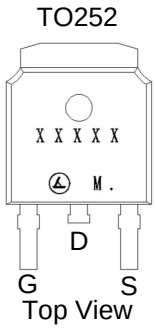
S-LP60N06D2

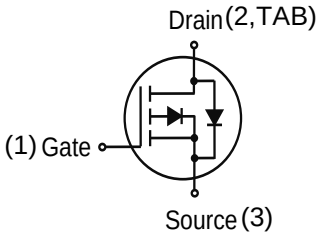
60V P-Channel Power MOSFET





1. FEATURES

- Low thermal impedance.
- Fast switching speed.
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S-prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



2. APPLICATIONS

- Load Switches
- DC/DC Conversion
- Motor Drives

3. DEVICE MARKING AND RESISTOR VALUES

Device	Marking	Shipping
S-LP60N06D2	P60N06D2	2500pcs/Tape&Reel

4. MAXIMUM RATINGS

Parameter		Symbol	Limits	Unit
Drain-to-Source Voltage		VDS	-60	V
Gate-to-Source Voltage		VGS	± 20	V
Continuous Drain Current	TC=25℃	ID	-59	A
	TC=100℃		-35	
Pulsed Drain Current (Note 2)		IDM	-236	A
Avalanche Current		IAS	-45.6	A
Avalanche Energy(L=0.1mH)		EAS	104	mJ
Power Dissipation	TC=25℃	PD	41	W
	TC=100℃		16	
Operating Junction and Storage Temperature Range		Tj/Tstg	-55~+150	℃

5. THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Junction-to-Ambient(Note 1)	RθJA	50	℃/W
Junction-to-Case	RθJC	3	

Note:1.Surface mounted on "1.5in x 1.5in" FR4 board using 1*1 in pad, 2 oz Cu.

2.Pulse width limited by maximum junction temperature.

6. ELECTRICAL CHARACTERISTICS (TJ= 25°C)

Characteristic		Symbol	Min.	Typ.	Max.	Unit
Static						
Drain–Source Breakdown Voltage (VGS = 0 V, ID = -250 μA)		V(BR)DSS	-60	-	-	V
Zero Gate Voltage Drain Current (VDS= -48 V, VGS= 0 V)		IDSS	-	-	-1	μA
Gate–Body Leakage Current (VDS = 0 V, VGS = ± 20 V)		IGSS	-	-	±100	nA
Gate Threshold Voltage (VDS = VGS, ID = -250 μA)		VGS(th)	-1	-	-3	V
Static Drain–Source On–State Resistance(Note 3) (VGS = -10 V, ID = -9 A) (VGS = -4.5 V, ID = -8 A)		RDS(on)	- -	- -	9.8 11.8	mΩ
Diode Forward Voltage (VGS = 0 V, IS = -3.6 A)		VSD	-	-	-1.2	V
Dynamic						
Input Capacitance	(VDS = - 30 V,VGS = 0 V,f = 1MHz)	Ciss	-	7044	-	pF
Output Capacitance		Coss	-	385	-	
Reverse Transfer Capacitance		Crss	-	355	-	
Total Gate Charge	(VDS = -30 V, VGS = -4.5 V, ID = -9 A)	Qg	-	66	-	nC
Gate-Source Charge		Qgs	-	18	-	
Gate-Drain Charge		Qgd	-	26	-	
Turn-On Delay Time	(VDS= -30 V,RL = 3.3 Ω,ID = -9 A,VGEN = -10 V,RGEN = 6 Ω)	td(on)	-	24	-	ns
Rise Time		tr	-	30	-	
Turn-Off Delay Time		td(off)	-	324	-	
Fall Time		tf	-	120	-	
Gate Resistance (VDS = 0 V,VGS = 0 V,f = 1.0MHz)		Rg	-	6	-	Ω

3.Pulse test: PW ⩽ 300us duty cycle ⩽ 2%.

7. ELECTRICAL CHARACTERISTICS CURVES

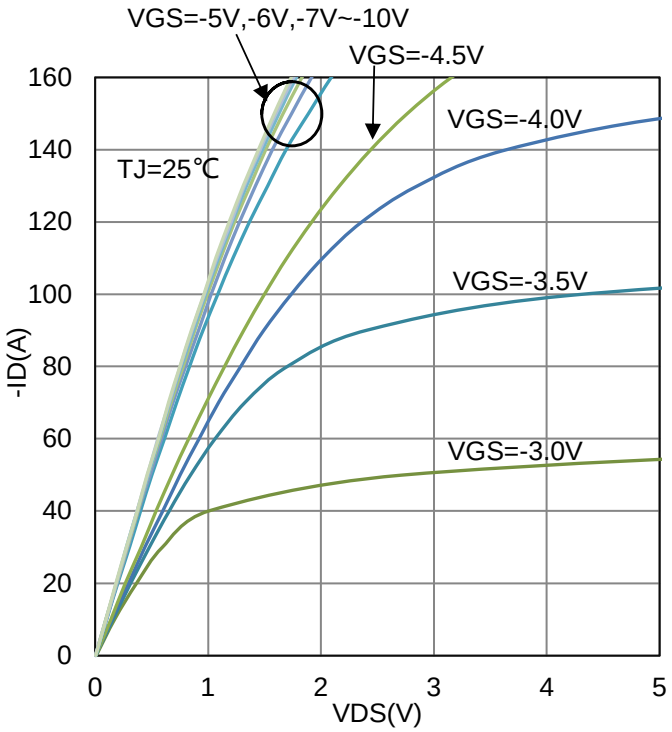


Figure 1.-ID vs. -VDS

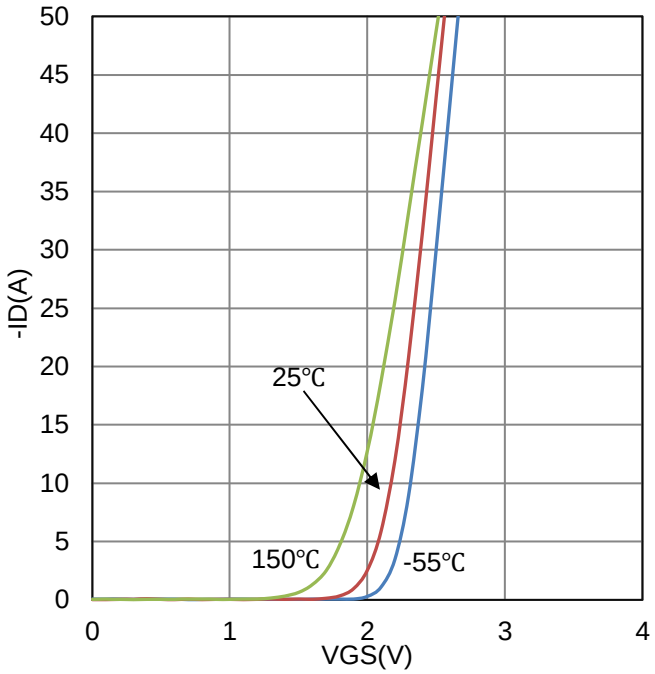


Figure 2.-ID vs. -VGS

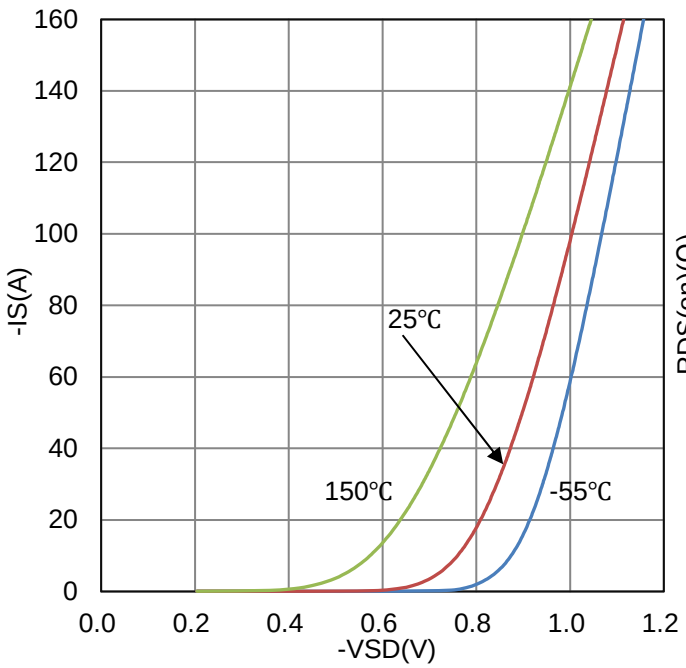


Figure 3.-IS vs. -VSD

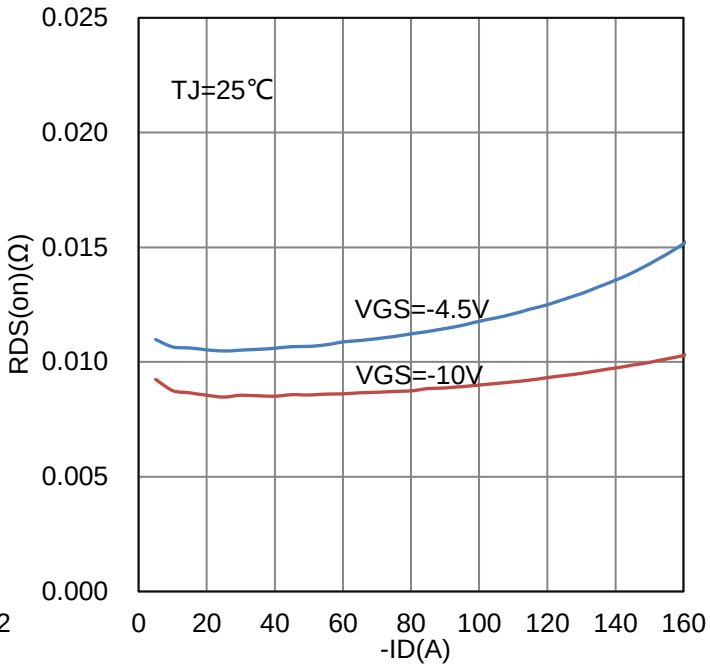


Figure 4.RDS(on) vs.-ID

7. ELECTRICAL CHARACTERISTICS CURVES(Con.)

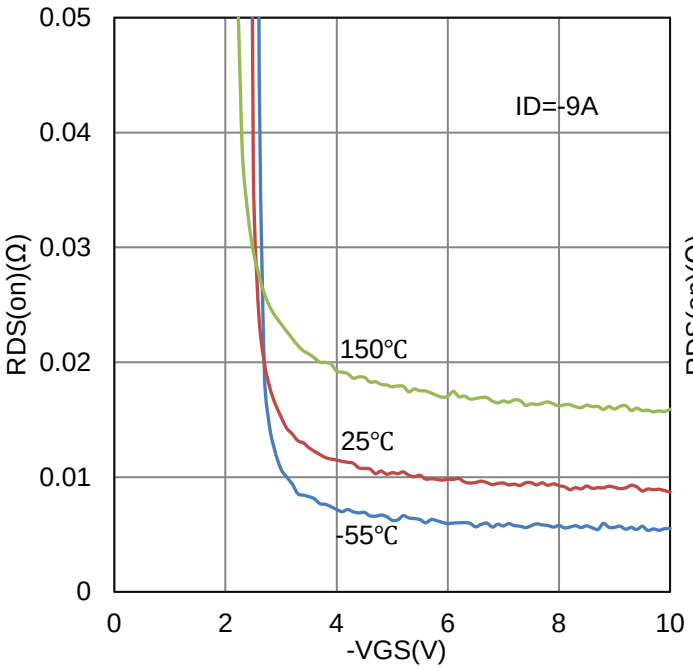


Figure 5.RDS(on) vs. -VGS

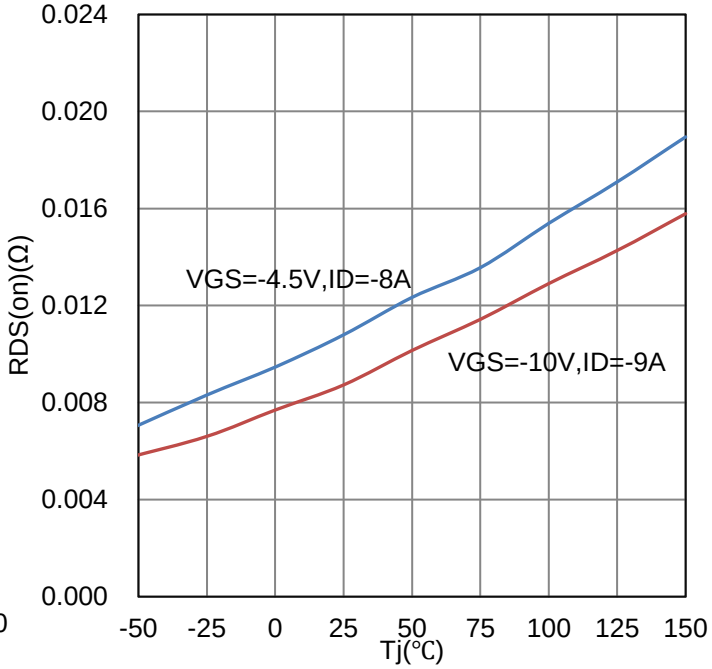


Figure 6.RDS(on) vs. Tj

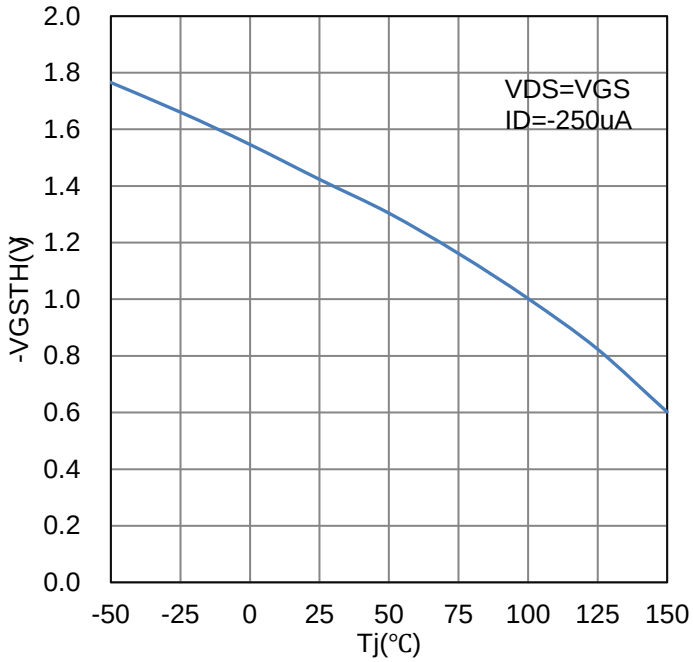


Figure 7.-VGSth vs. Tj

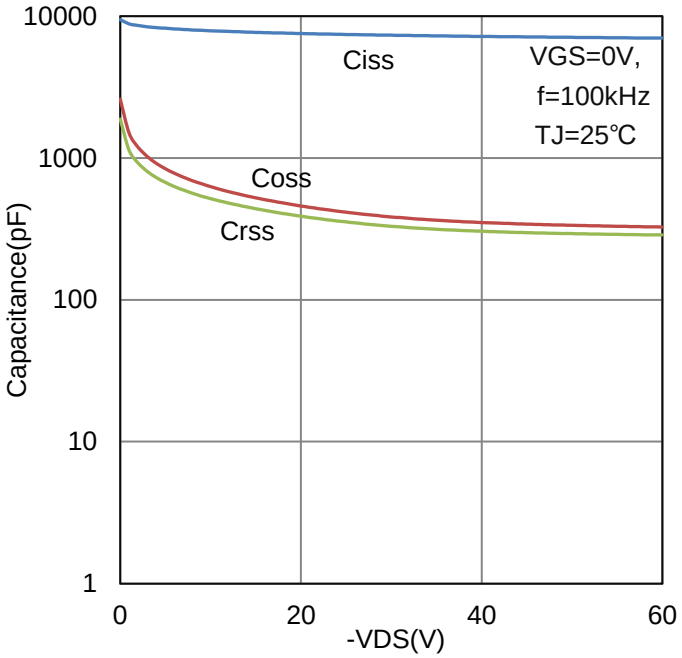


Figure 8.Capacitance

7. ELECTRICAL CHARACTERISTICS CURVES(Con.)

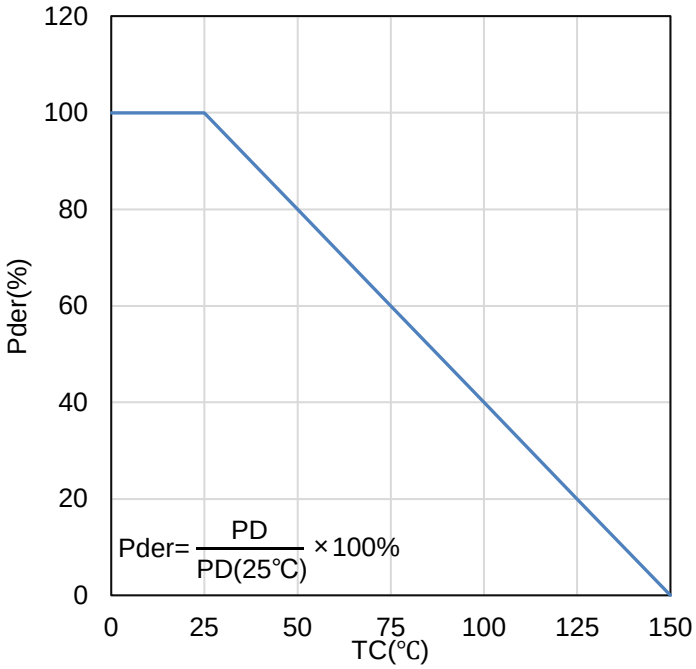


Figure 9.Normalized Derating Curve

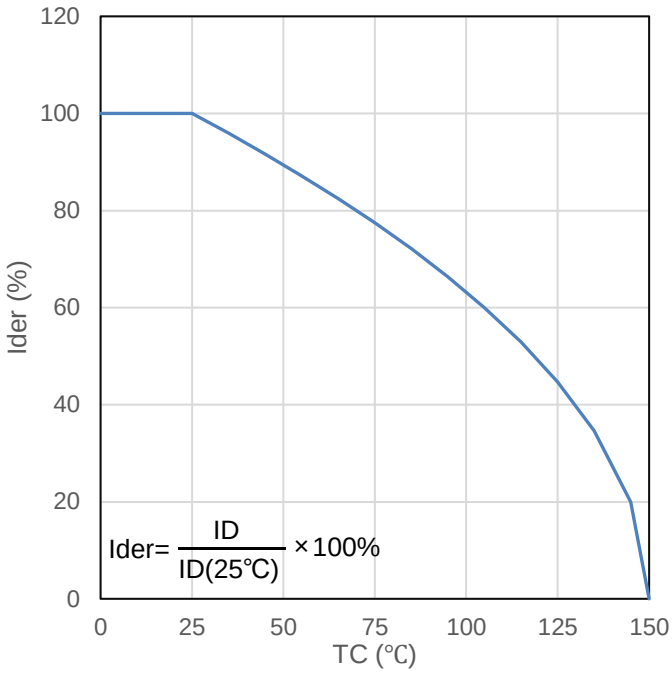


Figure 10.Normalized drain Current

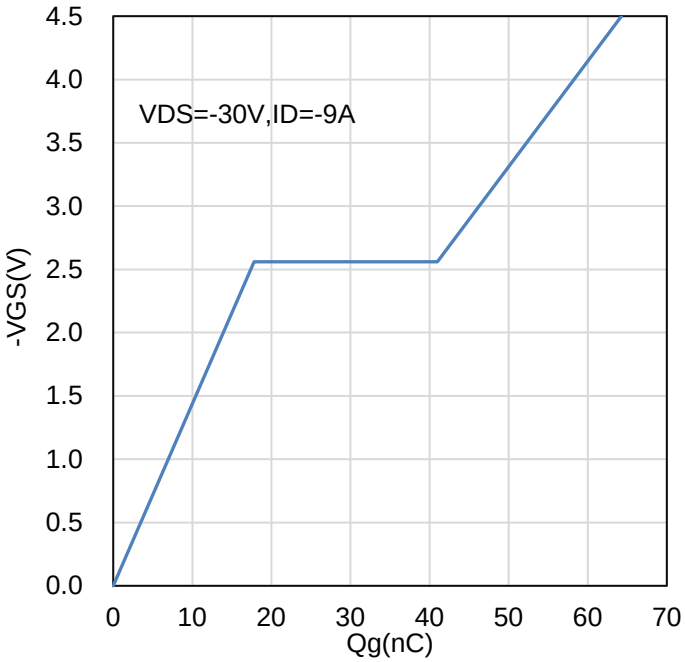


Figure 11.-VGS vs. Qg

7. ELECTRICAL CHARACTERISTICS CURVES(Con.)

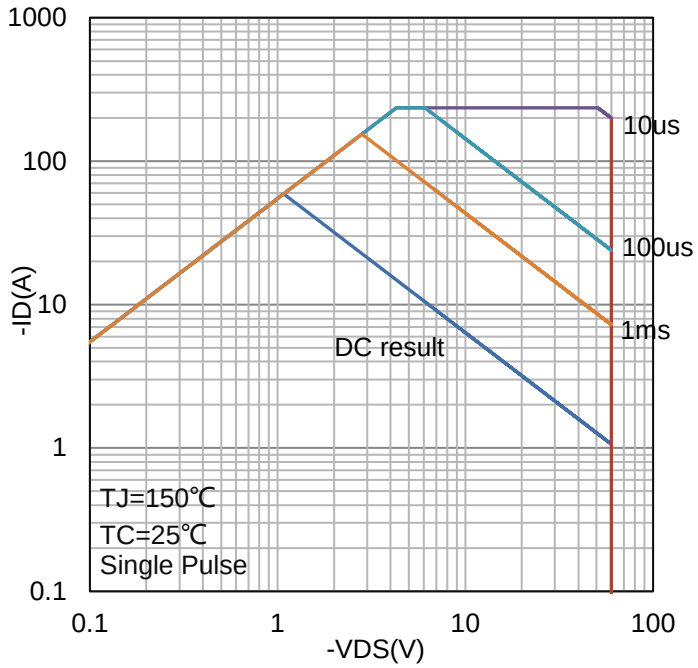


Figure 12.Safe Operating Area

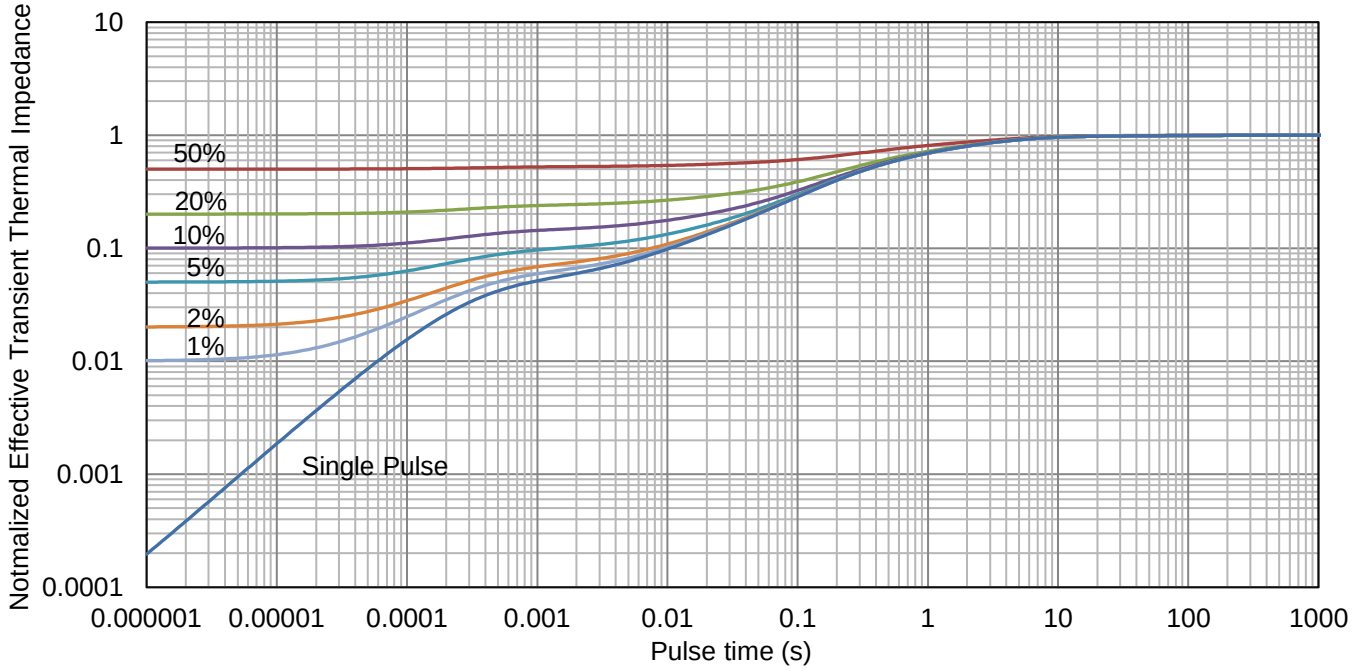
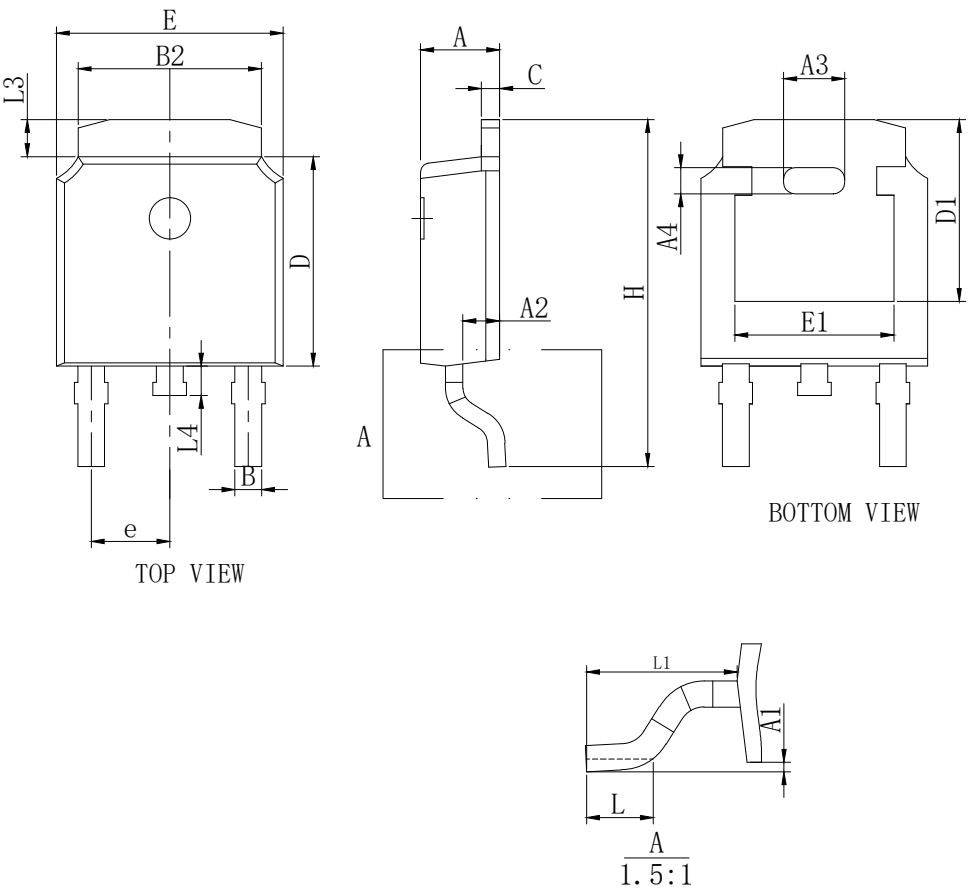


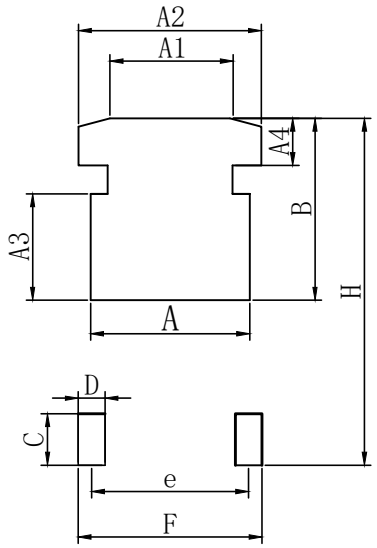
Figure 13.Thermal Response

8.OUTLINE AND DIMENSIONS



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	2.15	2.30	2.45
A1	0	-	0.20
A2	0.90	1.07	1.17
A3	1.58	1.78	1.98
A4	0.56	0.76	0.96
B	0.68	0.78	0.88
B2	5.20	5.33	5.46
C	0.49	-	0.58
D	5.90	6.10	6.30
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	4.83	5.03
e	2.286BSC		
H	9.8	10.10	10.4
L	1.09	1.29	1.49
L1	2.90REF		
L3	0.88	1.08	1.28
L4	0.55	0.80	1.05

9.SOLDERING FOOTPRINT



DIM	MIN(mm)
A	6.03
A1	4.50
A2	6.46
A3	4.10
A4	2.37
B	6.50
C	2.50
D	1.68
e	4.57(TYP)
H	12.35
F	6.25

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